Examiner-Initiated Interview Summary	Application No. 10/564,678	Applicant(s) ZHELTOV ET AL.
	Examiner Khai M. Nguyen	Art Unit 2819

All Participants:

(1) _Khai M. Nguyen_.

(2) _Sanjay S Gadkari_.

Status of Application: _____

(3) _____.

(4) _____.

Date of Interview: _2 February 2007_   **Time:** _arround 2:00 pm_

Type of Interview:
☒ Telephonic
☐ Video Conference
☐ Personal (Copy given to: ☐ Applicant   ☐ Applicant's representative)

Exhibit Shown or Demonstrated: ☐ Yes   ☒ No
If Yes, provide a brief description:

Part I.

Rejection(s) discussed:
none

Claims discussed:
_9_

Prior art documents discussed:
none

Part II.

SUBSTANCE OF INTERVIEW DESCRIBING THE GENERAL NATURE OF WHAT WAS DISCUSSED:
The examiner was authorized by attorney Gadkari to change claim 9 and its dependent claims 11 and 12 - see examiner's amendment attached herewith

Part III.

☒ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview directly resulted in the allowance of the application. The examiner will provide a written summary of the substance of the interview in the Notice of Allowability.

☒ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview did not result in resolution of all issues. A brief summary by the examiner appears in Part II above.

Nguyenkha 2/5/07
_____   _____
(Examiner/SPE Signature)   (Applicant/Applicant's Representative Signature – if appropriate)

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	
)	
Sergey N. Zheltov, et al.)	Examiner: Khai M. Nguyen
)	
Application No.: 10/564,678)	Art Unit: 2819
Docket No.: P16122)	
)	
Filed: 4/18/2006)	
)	
For: A Method Of Decoding Variable Length)	
Prefix Codes)	
)	

PROPOSED AMENDMENT

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to an interview with Examiner Nguyen on February 2, 2007, the following amendment is proposed:

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KN 2/5/07

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Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously amended) In a system for decoding variable length prefix codes in a bit stream, a method comprising:

reading, from the bit stream, a number of bits sufficient to store a longest variable length code of the system;

selecting a predetermined number of bits from the bits read; and

obtaining, from a data structure, in accordance with an actual value of the bits selected, at least a decoded value and a validity indicator associated with a variable length code; and

applying a prefix oriented decoding method to the bits initially read from the bit stream when the decoded value is indicated to be invalid.

2. (Cancelled)

3. (Original) The method of claim 1, wherein reading the number of bits comprises making the specified number of bits accessible for future operations.

4. (Original) The method of claim 1, wherein selecting the number of bits comprises making the specified number of bits accessible for future operations, faster than reading the same number of bits.

5. (Original) The method of claim 1, wherein the predetermined number of bits comprises the maximal number of bits to be used as an index to the data structure.

6. (Original) The method of claim 1, wherein the validity indicator indicates whether the decoded value is valid.

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7. (Original) The method of claim 1, wherein the data structure used to obtain at least the decoded value and validity indicator associated with a variable length code comprises a memory area containing at least the decoded value and validity indicator for each bit combination that can be formed from the predetermined number of bits.

8. (Previously amended) The method of claim 1, wherein the prefix oriented decoding method further comprises a method of variable length decoding that employs variable length code prefix properties during decoding.

9. (Currently amended) An article comprising: a tangible machine accessible medium having a plurality of machine readable instructions, which when accessed by a machine cause the machine to perform a method, the method comprising:

decoding of variable length prefix codes in a bit stream by

reading, from the bit stream, a number of bits sufficient to store a longest variable length code of the system;

selecting a predetermined number of bits from the bits read; and

obtaining, from a data structure, in accordance with an actual value of the bits selected, at least a decoded value and validity indicator associated with a variable length code; and

applying a prefix oriented decoding method to the bits initially read from the bit stream when the decoded value is indicated to be invalid.

10. (Cancelled)

11. (Currently Amended) The article of claim 9, wherein the method further comprises making the specified number of bits accessible for future operations.

12. (Currently Amended) The article of claim 9, wherein selecting the number of bits comprise further comprises making the specified number of bits accessible for future operations, faster than reading the same number of bits.

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13. (Original) The article of claim 9, wherein the predetermined number of bits comprises the maximal number of bits to be used as an index to the data structure.

14. (Original) The article of claim 9, wherein the validity indicator indicates whether the decoded value is valid.

15. (Original) The article of claim 9, wherein the data structure used to obtain at least the decoded value and validity indicator associated with a variable length code comprises a memory area containing at least the decoded value and validity indicator for each bit combination that can be formed from the predetermined number of bits.

16. (Previously amended) The article of claim 9, wherein prefix oriented decoding method further comprises a method of variable length decoding that employs variable length code prefix properties during decoding.

17. (Previously amended) A system for decoding variable length prefix codes in a bit stream, comprising:

logic to read from the bit stream a number of bits sufficient to store a longest variable length code of the system;

logic to select a predetermined number of bits from the bits read; and

logic to obtain from a data structure, in accordance with the actual value of the bits selected, at least a decoded value and a validity indicator associated with a variable length code; and

logic to apply a prefix oriented decoding method to the bits initially read from the bit stream when the decoded value is indicated to be invalid.

18. (Cancelled)

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19. (Original) The system of claim 17, wherein logic to read the number of bits comprises logic to make the specified number of bits accessible for future operations.

20. (Original) The system of claim 17, wherein logic to select the number of bits comprises logic to make the specified number of bits accessible for future operations, faster than logic to read the same number of bits.

21. (Original) The system of claim 17, wherein the predetermined number of bits comprises the maximal number of bits to be used as an index to the data structure.

22. (Original) The system of claim 17, wherein the validity indicator indicates whether the decoded value is valid.

23. (Original) The system of claim 17, wherein the data structure used to obtain at least the decoded value and validity indicator associated with a variable length code comprises a memory area containing at least the decoded value and validity indicator for each bit combination that can be formed from the predetermined number of bits.

24. (Previously amended) The system of claim 17, wherein prefix oriented decoding method further comprises a method of variable length decoding that employs variable length code prefix properties during decoding.

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Respectfully submitted,

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